



## Background

As technology continues to develop, electronic components continue to produce higher power densities. This becomes a present challenge that needs to be dealt with. New cooling solutions needs to be devised and to test these solutions a dedicated test bed is a necessity which is why we are making the high heat flux testbed.

## Goals & Objective

Objective 1 – *design, develop, and construct a testbed* capable of producing and dissipating high heat fluxes/loads.

The goal is to create a flux of 500-2000 W/cm<sup>2</sup> and have the flux concentrate onto a small area of  $1 \text{ cm}^2$ . The cooling system will then dissipate the heat and allow the system to maintain safe temperatures; in particular, the concentrated area will be kept at room temperature.

Objective 2- Create Test Plan and perform test sequence for data

# **Specifications**

- Vacuum compatible components
- Produce 2000 W/ $cm^2$
- Implement Safety Shutdown System
- Insulate entire system
- Heating area  $1 cm^2$

Week 1-3 Summarize project details and list design options

# **AFRL High Heat Flux Testbed**

Faculty Advisors: John LaRue, Khalid Rafique

#### Thermal Source: Resistors 2015-2016 Budget & Spending Arduino Shield \$100 \_Fiberglass, Frame, \$200 Arduino \$150 \_Resistors, \$100 Controller. Oxygen Free \$100 Thermocouples. , \$200 Copper Heat Block DAQ Device, \$500 Copper Cone, \$3,000 **Cold Plate Team Structure** 0 Project Manage **Richard Pham** Safety System Research Microchanne Calculations Lead Victoria Tien CAD Model Lead Lead Mellissa Perez & Roberto Trejo & Wendy Chu Wesley Dodge David Munoz Mike Morev Cooling System Alan Meza & Chris Lan Data Acquisition Elijah Pascual Research & CAD Jose Meza Hardware Design Material Properties & Costs Microcontroller & Ansys Yosuke Woodru Frame mount Connor Ball **Refrigerated Bath** Team Members **Richard Pham** Wendy Chu Yosuke Woodruff Victoria Tien Chris Lan Roberto Trejo Mike Morey Wesley Dodge **Connor Ball** Fall Timeline David Munoz Alan Meza **Elijah Pascual**

Week 6-8 **Detail calculations and Ansys** simulation

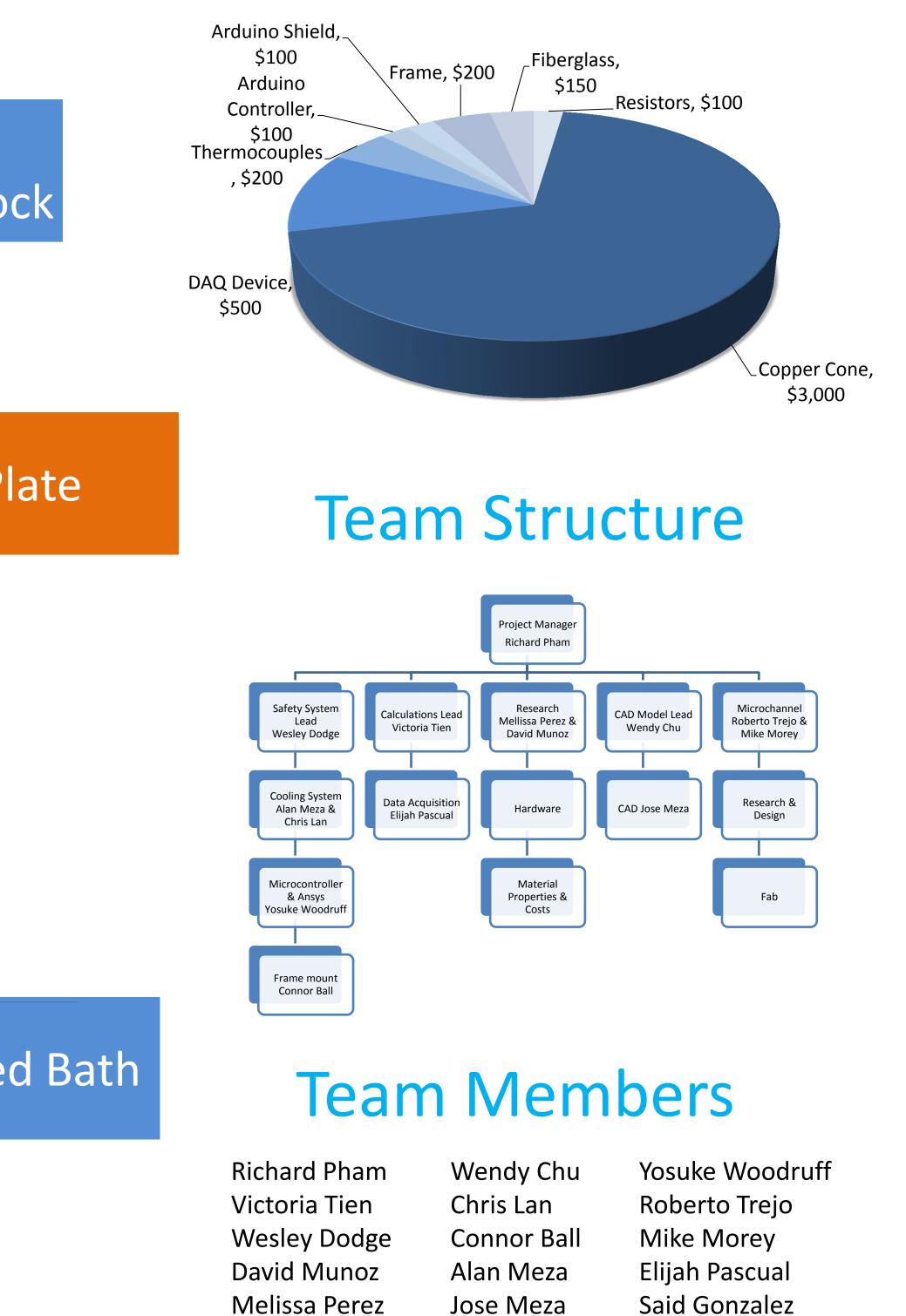
Week 10 Complete 1<sup>st</sup> cut detail design

Week 4-5 Schedule and hold PDR

Week 9 Hardware Research

 $\bigcirc$ 





**Contact Information:** 

**Richard Pham** Victoria Tien

rbpham@uci.eduvtien1@uci.edu

